



ST. JOSEPH'S DEGREE & PG COLLEGE

(Autonomous), Affiliated to Osmania University
Re-accredited by NAAC (3rd Cycle) with B++ Grade
King Koti Road, Hyderabad

Department of Physics & Electronics

Electronics

2nd Industry Academia Meet

24th February, 2023



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Agenda

Program: Electronics 2nd Industry-Academia meeting
Date: 24th February, 2023.
Venue: Conference Hall, 2nd floor.

1. Prayer by Mrs Mary Vinaya Sheela, Dean Academics, St. Joseph's Degree & PG College.
2. Welcoming the members.
3. Address by the Principal Fr. Dr. D. Sunder Reddy, St. Joseph's Degree & PG College.
4. Review of present electronics curriculum under CBCS
5. Implementation of UGC proposed curriculum based on common structure.
6. Generic Elective (GE) and Discipline Specific Electives (DSC).
7. Ability Enhancement Compulsory Courses (AECC) and Skill Enhancement Courses (SEC).
8. Certificate courses/ Internships/Project.
9. Any other information with the permission of the chair.
10. Vote of thanks by P. Johnson Babu.

The 2nd Industry Academia meet for Electronics course was held on 24th March, 2022 from 2.00 pm to 4.00 pm at Conference Hall. The following members attended the meeting.

1. Fr. Dr. D. Sunder Reddy
Principal
2. Fr. Prabhu Das.
Dean Academics
3. Mrs. Mary Vinaya Sheela
Dean Academics
4. Prof. C. P. Vardhani
Department of Physics, OU
5. Prof. Mohd. Shareefuddin
Department of Physics, OU
6. Dr. P. Chandra Shekar
Industry Expert, Director, Technique Design Group
7. Mr. Suresh Mehra
Industry Expert, Manager, Society of Sciences & technology. Hyd.
8. Dr. M. Sravan Kumar
HoD, Dept. of Physics & Electronics
9. Mr. R. Ashok Kumar
Assistant Professor
10. Mr. P. Johnson babu
Assistant Professor

Meeting started with the prayer by Mrs. Mary Vinaya Sheela madam, Dean Academics. Mr. R. Ashok Kumar introduced the members and given introductory talk on the importance of industry interaction for framing the syllabus. The following points are discussed.

1. As per the first year semester- I and II are fundamentals. The members felt that the syllabus is appropriate for the first year students.
2. Prof C.P. Vardhani suggested to add RF coupled amplifier and Tuned amplifier in Unit-3 of Semester – 3, Analog Circuits.
3. Dr. Chandra Shekar suggested to add DC – Dc Converter and Constant current source in Unit-2 of Semester – 3, Analog Circuits.
4. Members suggested to remove LC oscillator (Colpitts oscillator) and to add Crystal and Clap Oscillators of unit-4 of Semester – 3 Analog Circuits as it is similar to Hartley oscillator and also from experiments list
5. Board suggested that Semester – IV, Linear Integrated Circuits & Basic Communication Electronics syllabus is inappropriate and changes in the syllabus not required, as it can be implemented.
6. Dr. Chandra Shekar and Prof C.P. Vardhani suggested remove Universal building blocks and including excitation tables in unit - 2 of Semester – 5, Digital Electronics as they are not much useful at industry level.
7. Board suggested that to introduce implementation of logic circuit using and MOS and CMOS technologies in unit - 1 of Semester – 5, Digital Electronics.
8. Changes are not required in Unit -3 of Semester – 5; Digital Electronics suggested by the board members explanation of flip – flops along with the excitation tables as per the industry standards.
9. Members of Board suggested to include 16:1 Mux, 1:16 De – Mux and Schmitt trigger using simulator in practical paper – 5 of Semester-5
10. Prof.Md. Shareefuddin suggested to include parity generator and checker in the Lab in the place of Universal building blocks.
11. Members of Board suggested to include introduction of μ C8051 in place features in the syllabus of Paper-7 – DSE: 8051 Microcontroller.
12. Dr. P Chandra Shekar suggested to include A to D and D to A converters in the Practical list of 8051 Microcontroller.
13. Members of Board suggested to remove FDMA and CDMA and include serial bus RS485 in Unit – IV of Paper – 7 :Digital Communication

14. Members of Board felt that for the benefit of students introduce Mechatronics or IOT in place
Paper 8A: Digital Design using VHDL as it is tough for students.
15. Members of Board felt that introduction of DSD by Verilog also good for job opportunities to students.
16. Prof C.P. Vardhani and Prof.Md. Shareefuddin suggested to include a paper consisting of two units VHDL and two units of IOT based devices.

Inter Disciplinary Courses – Electronics

17. Members of Board suggested to introduce
 - i. Basic Electronics
 - ii. Computer Hardware electronics
 - iii. Mobile communication based software
 - iv. Hardware Mobile app based device control can be offered as generic electives papers in the Electronic course
18. New proposed programs suggested by Members of Board are
 - i. B.Sc. (Basic Electronics, Power Electronics and Data Science)
 - ii. B.Sc. (Basic Electronics, Robotics and Data Science)
 - iii. B.Sc. (Robotics)
19. Board suggested to go for integrated courses for five years or B.Sc. (Honors) course in Industrial automation.
20. Board suggestions about certificate courses or internship projects
 - i. Hobby Electronics
 - ii. Basic Robots
 - iii. Electronic testing
21. Board suggested to send students for the summer based internships or projects.

Meeting concluded with the presentation of formal vote of thanks by Mr. P. Johnson Babu.

2nd Industry Academia Meeting

Date: 24.02.2023⁷

Event Name: 2nd Industry Academia Meeting (IAM)
Electronics - 2023

Venue : Jubilee Hall

Timings : 2 PM to 4 PM

The dept. of physics & Electronics conducted the 2nd Industry Academia meeting (Electronics) - 2023 on 24.02.2023 at 2:00 PM in Jubilee Hall. The following members attended the meeting.

1. Fr. Dr. D. Sunder Reddy.
Principal

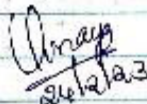


Principal

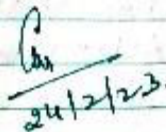
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2. Fr. Prabhu Das
Dean

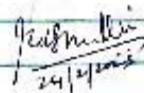
3. Mrs. Masry Vinaya Sheela
Dean Academics

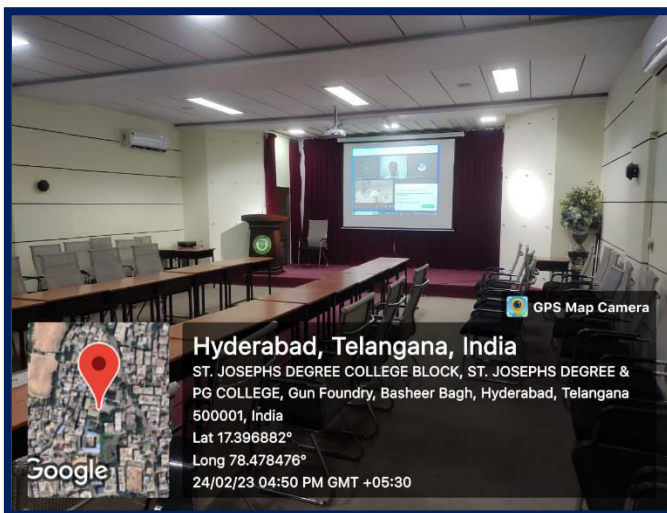


4. Prof. C.P. Vardhani
Academic Expert
Department of Physics, OU - 500 007


24/2/23

5. Prof. Md. Shareefuddin
Academic Expert
Department of Physics, OU - 500 007


24/2/23



Outcome: The suggestions provided by the industry-academia experts found very useful for designing the curriculum.