



ST. JOSEPH'S DEGREE & PG COLLEGE

(Autonomous), Affiliated to Osmania University
 Re-accredited by NAAC (3rd Cycle) with B⁺⁺ Grade
 King Koti Road, Hyderabad

B. Sc End Semester External Practical Examination Time-Table of Even Semester 2023 – 24

Date	Time	Subject	Group	Semester	Year	Room No./Floor
29 – 04 -2024 Monday	9.00 AM	Statistics VII (Design of Experiments, Index Number and Official Statistics)	MSCs	VI	III	3 rd Floor Lab2
		Physics-VIII (Basic Electronics)	MPCs	VI	III	Physics lab
	To	Electronics-VII (Digital Communication)	MECs	VI	III	Electronics lab
	12.00 PM	Computer science-IV (Java Programming)	MSCs	IV	II	3 rd Floor Computer Lab3
		(Backlog) Statistics – III Physics – III Electronics – III	MSCs MPCs MECs	III	All Years	3 rd Floor Computer Lab 3 Physics Lab Electronics Lab
	1:00 PM	Computer science-IV (Java Programming)	MPCs/ MECs	IV	II	3 rd Floor Computer Lab2
	To 4:00 PM	(Backlog) Computer Science – V & VI	MSCs MPCs MECs	V	All Years	3 rd Floor Computer Lab3
30-04-2024 Tuesday	9.00 AM	Mathematics-VII (Numerical Analysis)	MSCs	VI	III	508
		Statistics –IV (Statistical Inference)	MSCs	IV	II	3 rd Floor Computer Lab2
	To	(Backlog) Statistics-1 Physics -1 Electronics -1	MSCs MPCs MECs	I	All Years	3 rd Floor Computer Lab 3 Physics Lab Electronics Lab
	12.00 PM	(Backlog) Mathematics – III	MSCs	III	All Years	508
		(Backlog) Mathematics – V & VI	MPCs MECs	V	All Years	508
	1:00 PM To 4:00 PM	Physics-IV (Waves and Optics)	MPCs	IV	II	Physics lab
		Electronics-IV (Linear Integrated Circuits and Basic Communication Electronics)	MECs	IV	II	Electronics lab
		Mathematics-VII (Numerical Analysis)	MPCs/ MECs	VI	III	508
		(Backlog) Mathematics – III	MPCs MECs	III	All Years	508
		(Backlog) Mathematics – V & VI	MSCs	V	All Years	508

Date	Time	Subject	Group	Semester	Year	Room No./Floor
01-05-2024 Wednesday	9.00 AM	Computer Science-VII (Computer Networks)	MPCs/ MECs	VI	III	3 rd Floor Computer Lab3
		Statistics-II (Probability Distribution)	MSCs	II	I	3 rd Floor Computer Lab2
	To	Physics-II (Thermal Physics)	MPCs	II	I	Physics Lab
	12.00 PM	Electronics-II (Electronic Devices and Circuits)	MECs	II	I	Electronics Lab
		Mathematics-IV (Algebra)	MPCs/ MECs	IV	II	508
	1:00 PM	Mathematics-IV (Algebra)	MSCs	IV	II	508
	To	Computer Science-VII (Computer Networks)	MSCs	VI	III	3rdFloor Computer Lab3
4:00 PM	(Backlog) Mathematics – I	MSCs MPCs MECs	III	All Years	508	
02-05-2024 Thursday	9.00 AM	Statistics –VIII (SQC, Vital Statistics and Reliability)	MSCs	VI	III	3rdFloor Computer Lab2
		Physics-VII (Electro Magnetism)	MPCs	VI	III	Physics lab
	To	Electronics-VIII (Digital Design using VHDL)	MECs	VI	III	Electronics lab
	12.00 PM	Mathematics-II (Differential Equation)	MSCs	II	I	508
		Mathematics-II (Differential Equation)	MPCs/ MECs	II	I	508
	1:00 PM To 4:00 PM	(Backlog) Statistics- V & VI Physics – V& VI Electronics – V& VI	MSCs MPCs MECs	V	All Years	3 rd Floor Computer Lab 3 Physics Lab Electronics Lab
03-05-2024 Friday	9.00 AM	Mathematics-VIII (Number Theory)	MPCs/ MECs/ MSCs	VI	III	508
		To	Computer Science-II (Data Structures)	MPCs/ MECs	II	I
	12.00 PM	(Backlog) Computer Science – III	MSCs MPCs MECs	III	All Years	3 rd Floor Computer Lab3
		Mathematics-VIII (Solid Geometry)	MPCs/ MECs/ MSCs	VI	III	508
	1:00 PM To 4:00 PM	Computer Science-II (Data structures)	MSCs	II	I	3rdFloor Computer Lab2

Date	Time	Subject	Group	Semester	Year	Room No./Floor
04-05-2024 Saturday	9.00 AM To 12.00 PM	Computer Science-VIII (Big Data Analytics Using R)	MPCs/MECs/MSCs	VI	III	3rdFloor Computer Lab2
		Computer Science-VIII (Web Technologies)	MPCs/MECs/MSCs	VI	III	3rdFloor Lab3
	1:00 PM To 4:00 PM	(Backlog) Computer Science – I	MSCs MPCs MECs	I	All Years	3 rd Floor Computer Lab2
06-05-2024 Monday	9.00 AM to 12.00 PM	PROJECT	MPCs/MECs/MSCs	VI	III	508/Physics lab/Electronics Lab/3 rd Computer Lab

- ***Note: 1. All the students are instructed to come with the Hall Ticket.
2. Students must wear the College ID.
3. Practical Record with observation book must be submitted at the time of Examination.
4. All the backlog candidates are asked to attend the practical examination as per above mentioned time-table with practical record.
5. Project thesis must be submitted at the time of project viva-voce.
6. Project working modules of physics/electronics must be submitted at the time of project viva-voce for demo.
7. Cell phones are strictly prohibited in the practical examination lab/room.

HoDs.

1. Mrs. M. Kiran Jyothi :

J. P. S. 6/3/24.

2. Mr. D. Srinivas reddy:

D. S. 7/3/24

3. Mr. R. Ashok Kumar:

R. Ashok Kumar 6/3/24

R. Anita
Controller of Examination

Prof. R. Anita

Rev. Fr. Anthony Sagayaraja
Principal
6/3/24

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